

IN THE CLAIMS

Claims 1-9 (Canceled)

10. (New) A personal computer system, comprising:
- a personal computer main unit which outputs video signals;
 - a memory which stores the video signals outputted from said personal computer main unit;
 - a display panel which displays the video signals;
 - a processing circuit which converts a resolution of the video signals outputted from said personal computer main unit to a resolution of said display panel; and
 - a synchronizing circuit which synchronizes one of horizontal synchronous signals for the video signals outputted from said memory to one of horizontal synchronous signals for the video signals inputted to said memory each time a horizontal synchronous signal for the video signals inputted to said memory is detected M times and a horizontal synchronous signal for the video signals outputted from said memory is generated N times,
- wherein said M is not equal to said N.

11. (New) A personal computer system according to claim 10, wherein when said processing circuit enlarges the resolution of the video signals outputted from said personal computer main unit to the resolution of said display panel, M is smaller than N.

12. (New) A personal computer system according to claim 10, wherein the ratio of M to N corresponds to the ratio of the resolution of the video signals outputted from said personal computer main unit to the resolution of said display panel.

13. (New) A personal computer system according to claim 10, wherein said synchronizing circuit generates display timing signals which are used in said display panel.

14. (New) A personal computer system according to claim 13, wherein said synchronizing circuit synchronizes said display timing signal to said horizontal synchronous signals for the video signals outputted from said memory and said horizontal synchronous signals for the video signals inputted to said memory.

15. (New) A personal computer system according to claim 10, wherein said synchronizing circuit comprises:

a timing generating circuit which generates said horizontal synchronous signals for the video signals outputted from said memory, and

a memory control circuit which reads said video signals from said memory, in response to said horizontal synchronous signals generated by said timing generating circuit.

16. (New) A personal computer system according to claim 15, wherein said timing generating circuit receives said horizontal synchronous signals for the video signals inputted to said memory from said personal computer main unit.

17. (New) A personal computer system according to claim 16, wherein said timing generating circuit comprises:

a synchronizing circuit which synchronizes said horizontal synchronous signals for the video signals inputted to said memory to a reference basic clock, and

a generating circuit which generates internally an internal horizontal synchronous signals, and generates said horizontal synchronous signals for the video signals outputted

from said memory, with synthesizing said horizontal synchronous signals for the video signals inputted to said memory which are synchronized to said reference clocks, and said internal horizontal synchronous signals.

18. (New) A personal computer system according to claim 10, wherein said memory can store as many video signals as corresponding to a volume of one frame.

19. (New) A personal computer system according to claim 10, wherein said memory can store as many video signals as corresponding to a volume of two frames.